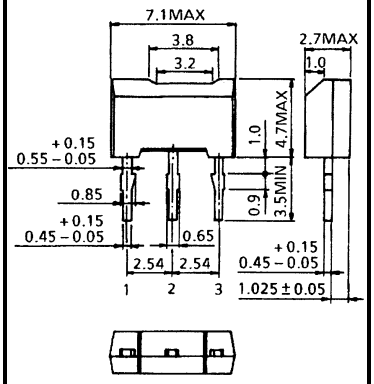
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process)

2SC3669

Power Amplifier Applications
Power Switching Applications

- Low collector saturation voltage: $V_{CE\ (sat)}$ = 0.5 V (max) (I_C = 1 A)
- High-speed switching: t_{stg} = 1.0 μs (typ.)
- Complementary to 2SA1429

Unit: mm



1. BASE
2. COLLECTOR
3. EMITTER

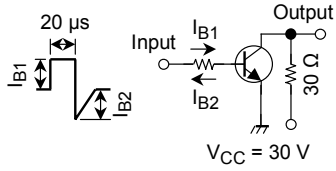
JEDEC	—
JEITA	—
TOSHIBA	2-7D101A

Weight: 0.2 g (typ.)

Maximum Ratings (Ta = 25°C)

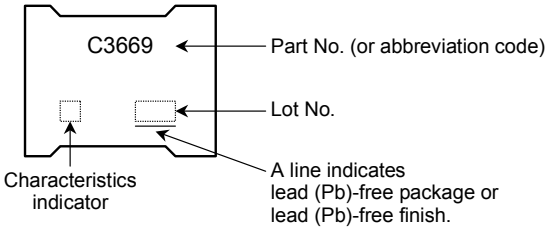
Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	80	V
Collector-emitter voltage	V_{CEO}	80	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I_C	2	A
Base current	I_B	1	A
Collector power dissipation	P_C	1000	mW
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	−55 to 150	°C

Electrical Characteristics (Ta = 25°C)

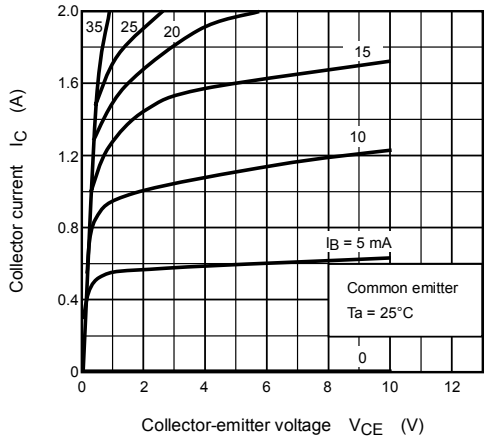
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	V_{CB} = 80 V, I_E = 0	—	—	1.0	μA
Emitter cut-off current		I_{EBO}	V_{EB} = 5 V, I_C = 0	—	—	1.0	μA
Collector-emitter breakdown voltage		$V_{(BR)\ CEO}$	I_C = 10 mA, I_B = 0	80	—	—	V
DC current gain		$h_{FE\ (1)}$ (Note)	V_{CE} = 2 V, I_C = 0.5 A	70	—	240	
		$h_{FE\ (2)}$	V_{CE} = 2 V, I_C = 1.5 A	40	—	—	
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	I_C = 1 A, I_B = 0.05 A	—	0.15	0.5	V
Base-emitter saturation voltage		$V_{BE\ (sat)}$	I_C = 1 A, I_B = 0.05 A	—	0.9	1.2	V
Transition frequency		f_T	V_{CE} = 2 V, I_C = 0.5 A	—	100	—	MHz
Collector output capacitance		C_{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	30	—	pF
Switching time	Turn-on time	t_{on}		—	0.2	—	μs
	Storage time	t_{stg}		—	1.0	—	
	Fall time	t_f	I_{B1} = −I_{B2} = 0.05 A, duty cycle ≤ 1%	—	0.2	—	

Note: $h_{FE\ (1)}$ classification O: 70 to 140, Y: 120 to 240

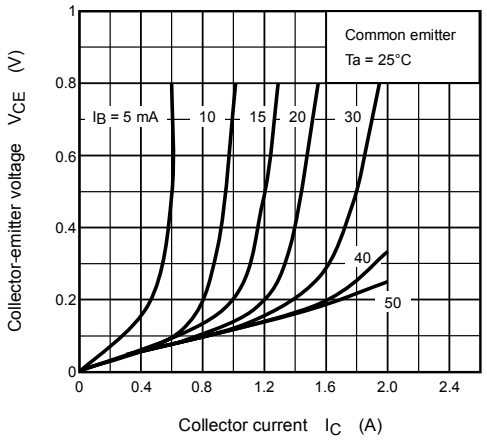
Marking


C3669
Part No. (or abbreviation code)
Lot No.
A line indicates
lead (Pb)-free package or
lead (Pb)-free finish.
Characteristics
indicator

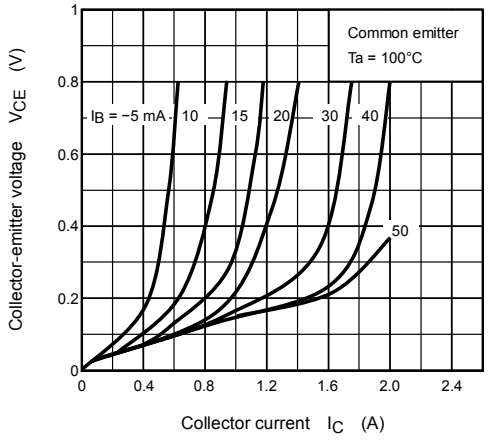
$I_C - V_{CE}$



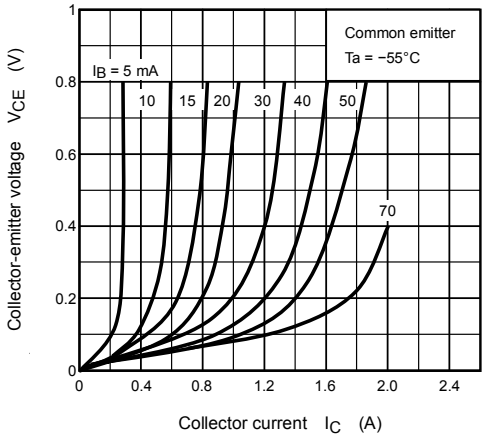
$V_{CE} - I_C$



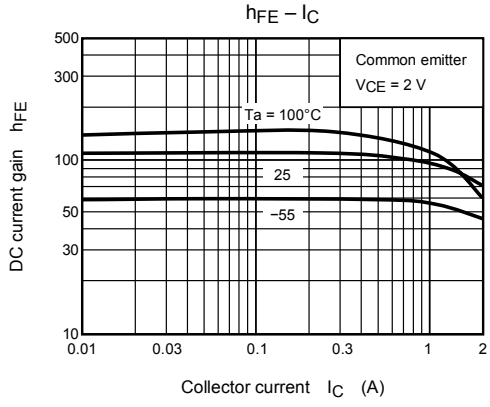
$V_{CE} - I_C$



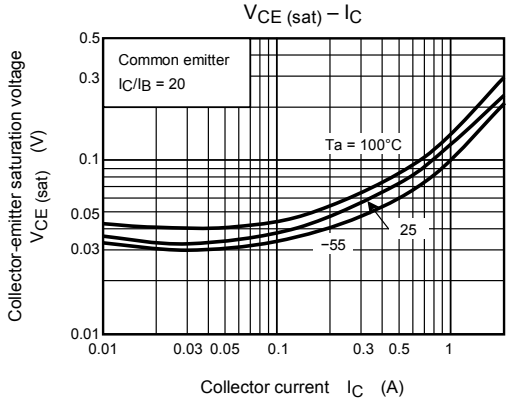
$V_{CE} - I_C$



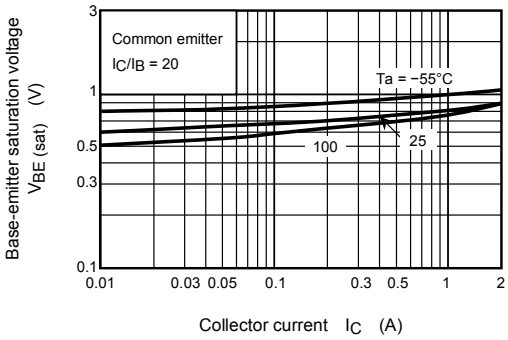
$h_{FE} - I_C$



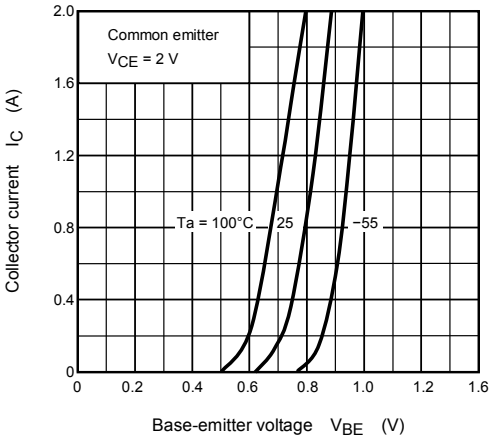
$V_{CE\ (sat)} - I_C$



$V_{BE\ (sat)} - I_C$



$I_C - V_{BE}$



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